

Appl. No. 09/776,951

Amdt. dated January 12, 2005

Reply to Office action of December 15, 2004

Amendments to the Specification:

Page 4, lines 13-17:

The connections of the capacitive load structures to the actual signal transmission lines of the networks must not be covered with solder. This is in order to permit performance of laser cutting which is known in the art in printed circuit board production.

Page 6, lines 24-25:

~~Other features which are considered as characteristic for the invention are set forth in the appended claims.~~

Page 18, lines 1-10:

Abstract of the Disclosure:

~~The invention relates to a~~ A method and ~~to a device for~~ adapting/tuning adapt/tune signal transit times on line systems or networks between integrated circuits which are mounted on printed circuit boards. Fine tuning of differences in transit times can be easily carried out by capacitive load structures disposed on conductor tracks ~~(3-9)~~ in the vicinity of chip housings of the integrated circuits on the printed circuit boards by disconnecting connecting lines ~~(20, 21)~~ using a cutting laser ~~(Fig. 1B)~~.